# On Iterative Decoding in Some Existing Systems

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Abstract—Iterative decoding is used to achieve backward compatible performance improvement in several existing systems. Concatenated coding and iterative decoding are first set up using composite mappings, so that various applications in digital communication and recording can be described in a concise and uniform manner. An ambiguity zone detection (AZD) based iterative decoder, operating on generalized erasures, is described as an alternative for concatenated systems where turbo decoding cannot be performed. Described iterative decoding techniques are then applied to selected wireless communication and digital recording systems. Simulation results and utilization of decoding gains are briefly discussed.

*Index Terms*—Concatenated coding, digital recording systems, iterative decoding, wireless communication systems.

### I. INTRODUCTION

**PRIMARY** objective of any digital communication system is to transmit information at the maximum possible rate and receive it with minimum distortion or errors. The concept of capacity from channel coding theorem upper-bounds the transmission speed at which reliable communication is possible on a large class of channels. Because only long codewords can guarantee small decoding errors at high transmission rates and the codebook size increases exponentially with the codeword length, finding the message best matching received channel data becomes a difficult task. As a result, finding good codes with *practical decoders* turned out to be the main challenge in achieving reliable transmission at rates close to the channel capacity.

The most powerful channel coding techniques known today are based on *concatenation*, that is combination of smaller encoders at the transmitter. The data are encoded by a cascade of encoders and this in effect creates a powerful code of large block length. The concept appeared originally in Elias' product codes [1], was conceptually utilized in Gallager's low density parity check (LDPC) codes [2], and got named and developed to schemes with many practical applications in Forney's con-

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catenated codes [3]. Optimal decoding of concatenated codes on most channels would be, in most cases, too complex to be practical so suboptimal decoders have been proposed in the literature and got applied in practical systems. The basic idea is to utilize decoders of constituent encoders and decode noisy channel data either in one sequence of steps or repetitively. The latter approach, in particular *turbo decoding* principle proposed originally for parallel concatenated convolutional codes by Berrou *et al.* [4], achieved unprecedented performance only a few tenths of a dB from capacity of the additive white Gaussian channel. Further studies showed successful use of turbo decoding principle in many concatenated systems of practical interest [5]–[8].

The structure of this paper is as follows. Concatenated encoding techniques are first described, viewing encoders and decoders as mappings. Using this setting, iterative decoders for systems with concatenated structure are reviewed, including an ambiguity zone detection (AZD) based iterative decoder. The iterative decoders are then applied to a recording system, a GSM system, and a wireless system with diversity. Simulation results and discussion of performance improvement utilization are presented.

#### II. CODES FROM CONCATENATION AND THEIR DECODING

### A. Preliminaries

In a digital communication system *encoder* is a mapping from a set of messages  $\mathcal{M}$  to a codebook  $\mathcal{C}$ 

$$f: \mathcal{M} \to \mathcal{C} \subseteq \mathcal{S} \tag{1}$$

where C is a subset of sequences S which are allowed as inputs to the channel or the next stage encoder. Similarly, *decoder* is a mapping from the set of received channel data  $\mathcal{R}$  into a decision set  $\mathcal{D}$ 

$$\varphi \colon \mathcal{R} \to \mathcal{D}.$$
 (2)

Encoders in this setting are devices that introduce a *redundancy* and/or *memory* constraint, as the data pass through them in digital communication, e.g., error-correcting codes, run-length limited codes, or constrained channels but may also interleave (permute) the symbols prior or after application of the constraint. Given encoder or decoder can be implemented using different algorithms, e.g., a minimum distance decoder for a binary linear block code on a binary symmetric channel can be implemented via exhaustive search, syndrome decoding algorithm, or Viterbi algorithm that uses code's trellis structure.

Unless stated otherwise, we will assume binary encoders, i.e.,  $\mathcal{M} = \{0, 1\}^k$  and  $\mathcal{C} \subseteq \{0, 1\}^n$ . Log-APP ratios (log-arithmic ratios of *a posteriori* probabilities) are defined for bi-

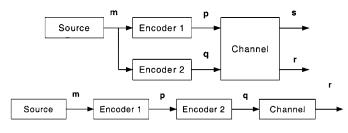


Fig. 1. Schematic block diagrams of two encoders concatenated in (a) parallel (b) series. (Note that for notational convenience, interleavers are considered to be parts of encoding maps.)

nary random variables and vectors, e.g., for binary vector  $\mathbf{m}$  and channel data  $\mathbf{r}$ 

$$\Lambda(\mathbf{m} \mid \mathbf{r}) = \left(\frac{P(m_1 = 1 \mid \mathbf{r})}{P(m_1 = 0 \mid \mathbf{r})}, \frac{P(m_2 = 1 \mid \mathbf{r})}{P(m_2 = 0 \mid \mathbf{r})}, \dots, \frac{P(m_k = 1 \mid \mathbf{r})}{P(m_k = 0 \mid \mathbf{r})}\right).$$
(3)

Decisions about message bits can be then formed via thresholding, denoted by the use of unit-step function

$$u(x) = \begin{cases} 1, & \text{if } x \ge 0\\ 0, & \text{otherwise} \end{cases}$$

or its vector version  $u(\mathbf{x}) = (u(x_1), u(x_2), \dots, u(x_n)).$ 

## B. Encoder Concatenation

Concatenation of encoders in a communications system may occur because of the original transmitter or receiver architecture, or due to the channel over which the data are transmitted in a practical communication system. For instance, a serial concatenation may result from cascading several encoders at the transmitter, e.g., a product code, followed by a run-length limited code and a partial response channel, as often found in a digital recording system. It may also be due to a channel subject to multipath fading or ISI in a coded system, as is the case in the GSM system [9]. Parallel concatenation can be a result of diversity, when multiple transmissions and/or receptions occur in the time, space, or frequency domain. It can be also be a result of an automatic repeat request (ARQ) protocol when another set of channel data about the same source message becomes available at the receiver. A hybrid concatenation occurs if a combination of the aforementioned factors is present in a system, as in the message watch paging system of Seiko Communications.

The parallel concatenation of two encoders, shown in Fig. 1(a), can be formally written as

$$f(\mathbf{m}) = (f_1(\mathbf{m}), f_2(\mathbf{m})) \tag{4}$$

where

$$f_1: \mathcal{M} \to \mathcal{P} \subseteq \mathcal{X}$$
$$f_2: \mathcal{M} \to \mathcal{Q} \subseteq \mathcal{Y}.$$
 (5)

Similarly, serial concatenation of two encoders, depicted in Fig. 1(b), can be formally written as a composite mapping

$$f(\mathbf{m}) = f_2(f_1(\mathbf{m})) \tag{6}$$

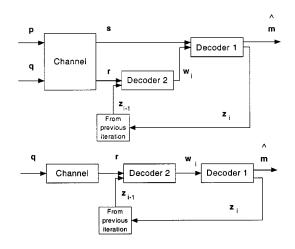


Fig. 2. Schematic block diagrams of iterative decoders for two codes concatenated in (a) parallel (b) series.

where outer and inner encoders are described as

$$f_1: \mathcal{M} \to \mathcal{P} \subseteq \mathcal{X}$$
$$f_2: \mathcal{X} \to \mathcal{Q} \subseteq \mathcal{Y}. \tag{7}$$

The encoder concatenation concept and definitions can be easily extended to cases where we deal with more encoders.

For instance, the original turbo encoder from [4] can be rewritten in this setting with (4) and (5) as follows.  $\mathcal{M} = \{0; 1\}^k$  and encoder  $f_1$  is based on the nonuniform interleaver and applying the rate 1/1 recursive convolutional code (no systematic part,  $\mathcal{X} = \{0, 1\}^k$ ). Encoder  $f_2$  is based on systematic encoding with the recursive convolutional code  $(\mathcal{Y} = \{0; 1\}^{2k})$ .

# C. Iterative Decoding of Concatenated Codes

1) Turbo Decoding: For data encoded by concatenated encoders and corrupted by channel noise, finding an optimal decoder is usually difficult for commonly used criteria, e.g., minimization of sequence error probability (MLSE) or bit error probability (MAP criteria). Except for a few simple cases, such a decoder constructed by conventional means would be too complex to be practical. Turbo decoding is the best suboptimal scheme known today. An iterative turbo decoder is schematically shown in Fig. 2(a) in case of two encoders concatenated in parallel. It connects the two subdecoders in a loop, where soft decision decoding information is gradually refined as it is passed between the decoders and can be expressed as

$$\mathbf{w}_{i} = \varphi_{2} \left( \mathbf{r}, \, \mathbf{z}_{i-1} \right) - \mathbf{z}_{i-1}$$
$$\mathbf{z}_{i} = \varphi_{1}(\mathbf{s}, \, \mathbf{w}_{i}) - \mathbf{w}_{i}$$
(8)

with the initial condition chosen to be  $\mathbf{z}_0 = (0, 0, \dots, 0)$ . The decoding vector functions  $\varphi_1$  and  $\varphi_2$  are defined using log-APP ratios

$$\varphi_2(\mathbf{r}, \mathbf{z}_{i-1}) = \Lambda(\mathbf{m} \mid \mathbf{r}, \mathbf{z} = \mathbf{z}_{i-1})$$
(9)

$$\varphi_1(\mathbf{s}, \mathbf{w}_i) = \Lambda(\mathbf{m} \mid \mathbf{s}, \mathbf{w} = \mathbf{w}_i) \tag{10}$$

where vectors z and w, termed *extrinsic information*, are treated as if they contained independent probabilities (log-ratios) about bits in message **m**. [To achieve this formally, **z** and **w** can be defined as realizations of a fictitious Gaussian random vector with unit covariance matrix and mean  $\mathbf{m} - (1/2, 1/2, ..., 1/2)$ .] Decisions about the source message bits after the *i*th iteration can be made by forming hard decisions at Decoder 1, e.g., via thresholding

$$\hat{\mathbf{m}} = u(\Lambda(\mathbf{m} \mid \mathbf{s}, \mathbf{w}_i)) \tag{11}$$

where u(.) is the unit step function defined in Section II-A.

Turbo decoding from (8), when applied to decoding of serially concatenated encoders from (6), (7), is schematically shown in Fig. 2(b). It can be formally described as

$$\mathbf{w}_{i} = \varphi_{2} \left( \mathbf{r}, \mathbf{z}_{i-1} \right) - \mathbf{z}_{i-1}$$
$$\mathbf{z}_{i} = \varphi_{1} \left( \mathbf{w}_{i} \right) - \mathbf{w}_{i}$$
(12)

where the initial condition is again chosen to be  $\mathbf{z}_0 = (0, 0, \dots, 0)$ . The decoding functions  $\varphi_1$  and  $\varphi_2$  in this setting are defined as

$$\varphi_2(\mathbf{r}, \mathbf{z}_{i-1}) = \Lambda(\mathbf{x} \mid \mathbf{r}, \mathbf{z} = \mathbf{z}_{i-1})$$
(13)

$$\varphi_1(\mathbf{w}_i) = \Lambda(\mathbf{p} \mid \mathbf{w} = \mathbf{w}_i) \tag{14}$$

where vectors  $\mathbf{z}$  and  $\mathbf{w}$  are treated as if they contained independent probabilities (log-ratios) about coded bits in  $\mathbf{p} = f_1(\mathbf{m})$ . The decision about the message bits can be again determined by making hard decisions at Decoder 1, e.g.

$$\hat{\mathbf{m}} = u(\Lambda(\mathbf{m} \mid \mathbf{w} = \mathbf{w}_i)). \tag{15}$$

For a memoryless channel [e.g., additive white Gaussian noise (AWGN)], the decoding functions  $\varphi_1$  and  $\varphi_2$  can be implemented using the BCJR algorithm. As the iterative process tends to converge reasonably fast, the scheme has practical storage, computational and implementation complexities for encoders with trellis structure of reasonable size. The complexity of the turbo decoder can be lowered by using approximate soft output Viterbi algorithm (SOVA) algorithm [10] in implementing the decoding functions, though the performance slightly worsens.

2) Iterative AZD Based Decoder: This section gives a general description of the AZD based iterative decoder whose initial version has been proposed by Kobayashi and Bajcsy [11]. This decoder builds on the idea of the ambiguity based detection of Kobayashi and Tang [12] in order to obtain a low complexity iterative decoder based on generalized erasures. The technique can be seen as an alternative to turbo decoding, especially in systems where original system design, stringent limitations on the overall receiver complexity or limited availability of practical soft decision decoders for some codes (e.g., Reed Solomon codes) may prevent turbo decoding. The AZD based iterative decoder can be seen as an application of the bisection method principle to suitably formulated decoding problem for concatenated encoders, while the turbo decoder can be seen as an application of the functional iteration technique to decoding in concatenated systems [13]. It is important to note that for channel with pure erasures, independent studies have been done on concatenated code design and iterative decoding by Luby et al. [14], [15].

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The proposed AZD based decoding process is similar to crossword puzzle solving and its spirit is illustrated in the following simple example, while the remainder of the section gives its formal description and addresses implementation issues.

*Example 2.1:* A  $3 \times 3$  binary crossword puzzle, shown on the left of (16) is supposed to be solved, i.e., all unknown binary letters denoted by *E*s are to be resolved. Hints state that the sum of symbols is even in each of the first two rows as well as columns. We attempt to resolve the unknown symbols by iteratively applying the row and column constraints.

Iteration 1 proceeds as follows

$$\begin{array}{ccc} 0 & E_{1} & 0 \\ E_{2} & E_{3} & 0 \\ E_{4} & 0 \end{array} \end{array} \xrightarrow{\text{columns}} \begin{bmatrix} 0 & E_{1} & 0 \\ E_{2} & E_{3} & 0 \\ E_{4} & 0 \end{array} \end{bmatrix} \xrightarrow{\text{rows}} \begin{bmatrix} 0 & 0 & 0 \\ E_{2} & E_{3} & 0 \\ E_{4} & 0 \end{bmatrix} .$$
 (16)

No erased digits in the initial array can be resolved using the column-wise hints without possibly making an error, i.e., two erasures in column 1 could be resolved either as  $E_2 = E_4 = 0$  or as  $E_2 = E_4 = 1$ . The same applies to  $E_1$ ,  $E_3$  in column 2. In the second step of iteration 1, row-wise constraints are used and  $E_1 = 0$  is resolved based on the first row constraint.  $E_2$  and  $E_3$  still cannot be resolved because they could either both be 1 or 0, and  $E_4$  is not involved in any row constraint.

*Iteration 2* starts with the rightmost expression of (16) and tries to resolve the remaining erasures using again the constraints on columns and rows, i.e.

$$\begin{bmatrix} 0 & 0 & 0 \\ E_2 & E_3 & 0 \\ E_4 & 0 \end{bmatrix} \xrightarrow{\text{columns}} \begin{bmatrix} 0 & 0 & 0 \\ E_2 & 0 & 0 \\ E_4 & 0 \end{bmatrix}$$
$$\xrightarrow{\text{rows}} \begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ E_4 & 0 \end{bmatrix}. \quad (17)$$

First, single erasure in column 2 is resolved as  $E_3 = 0$ , whereas two erasures in column 1 cannot be uniquely resolved. Applying row-wise constraints then reveals  $E_2 = 0$  and the next iteration can be started.

Iteration 3 starts by using the column constraints and after  $E_4$  is found to be 0

$$\begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ E_4 & 0 \end{bmatrix} \xrightarrow{\text{columns}} \begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 0 \end{bmatrix}$$
(18)

the iterative process stops as all erasures in the original array have been resolved.

The described iterative principle can be applied to decoding of two encoders concatenated in parallel (4), (5). Schematic block diagram of AZD based iterative decoder is shown in Fig. 2(a), and it can be formally written as

$$\mathbf{w}_{i} = \varphi_{2,i} \left( \mathbf{r}, \mathbf{z}_{i-1} \right)$$
$$\mathbf{z}_{i} = \varphi_{1,i} \left( \mathbf{s}, \mathbf{w}_{i} \right)$$
(19)

starting with the initial condition  $\mathbf{z}_0 = (E, E, \dots, E)$ . Assuming for simplicity binary encoders and AWGN channel, vector decoding functions  $\varphi_2$ ,  $\varphi_1$  have the *j*th coordinate of their output defined as

$$\varphi_{2,i}(\mathbf{r}, \mathbf{z})_j = \begin{cases} 0 \text{ (or 1)}, & \text{if } m_j = 0 \text{ (or 1)} \\ & \text{for all } \mathbf{m} \in \mathcal{M}_2(\mathbf{z}, t_{2,i}) \\ E, & \text{otherwise} \end{cases}$$

$$\varphi_{1,i}(\mathbf{s}, \mathbf{w})_j = \begin{cases} 0 \text{ (or 1)}, & \text{if } m_j = 0 \text{ (or 1)} \\ & \text{for all } \mathbf{m} \in \mathcal{M}_1(\mathbf{w}, t_{1,i}) \\ E, & \text{otherwise} \end{cases}$$

utilizing the set of possible candidate messages at each decoder

$$\mathcal{M}_{2}(\mathbf{z}, t_{2}) = \{ \mathbf{m} \in \mathcal{M} : ||f_{2}(\mathbf{m}) - \mathbf{r}||^{2} < t_{2} \text{ and } m_{l} = z_{l},$$
for all  $l$  s.t.  $z_{l} \neq E \}$ 
$$\mathcal{M}_{1}(\mathbf{w}, t_{1}) = \{ \mathbf{m} \in \mathcal{M} : ||f_{1}(\mathbf{m}) - \mathbf{s}||^{2} < t_{1} \text{ and } m_{l} = w_{l},$$
for all  $l$  s.t.  $w_{l} \neq E \}.$  (22)

Thresholds parameters  $t_1$ ,  $t_2$  are chosen based the actual code and channel parameters and may be gradually tightened as the decoding proceeds [denoted by  $t_{1,i}$  and  $t_{2,i}$  in (21) and (20)]. The decoding decision  $\hat{\mathbf{m}}$  about the transmitted message is contained in  $\mathbf{z}_i$  after each iteration.

The AZD based iterative decoder proceeds similarly in case of two serially concatenated encoders from (6) and (7). It is schematically shown in Fig. 2(b) and can be written as

$$\mathbf{w}_{i} = \varphi_{2, i} \left( \mathbf{r}, \mathbf{z}_{i-1} \right)$$
$$\mathbf{z}_{i} = \varphi_{1, i} (\mathbf{w}_{i})$$
(23)

starting with  $\mathbf{z}_0 = (E, E, E, E, \dots, E)$ . Assuming again binary encoders and AWGN channel, vector decoding functions  $\varphi_2$ ,  $\varphi_1$  have the *j*th coordinate of their output defined as

$$\varphi_{2,i}(\mathbf{r}, \mathbf{z})_j = \begin{cases} 0 \text{ (or 1)}, & \text{if } x_j = 0 \text{ (or 1)} \\ & \text{for all } \mathbf{x} \in \mathcal{X}_2(\mathbf{z}, t_{2,i}) \\ E, & \text{otherwise} \end{cases}$$
(24)

$$\varphi_{1,i}(\mathbf{w})_j = \begin{cases} 0 \text{ (or 1)}, & \text{if } p_j = 0 \text{ (or 1)} \\ & \text{for all } \mathbf{m} \in \mathcal{P}_1(\mathbf{w}) \\ E, & \text{otherwise} \end{cases}$$
(25)

where the candidate sets at each decoder are defined as

$$\mathcal{X}_{2}(\mathbf{z}, t_{2}) = \left\{ \mathbf{x} \in \mathcal{X} \colon \|f_{2}(\mathbf{x}) - \mathbf{r}\|^{2} < t_{2} \text{ and } x_{l} = z_{l}, \\ \text{for all } l \text{ s.t. } z_{l} \neq E \right\}$$
$$\mathcal{P}_{1}(\mathbf{w}) = \left\{ \mathbf{p} \in \mathcal{P} \colon p_{l} = w_{l} \text{ for all } l \text{ s.t. } w_{l} \neq E \right\}.$$
(26)

If  $f_1$  is a systematic encoder, the decoding decision  $\hat{\mathbf{m}}$  about the transmitted source message is contained in  $\mathbf{z}_i$  each iteration. Otherwise, it can be formed at Decoder 1 coordinate wise, i.e.,

$$\hat{m}_j = \begin{cases} 0 \text{ (or 1)}, & \text{if } m_j = 0 \text{ (or 1)}, \\ & \text{for all } \mathbf{m} \in f_1^{-1}(\mathcal{P}_1(\mathbf{z}, t_{1,i})) \\ E, & \text{otherwise.} \end{cases}$$
(27)

To implement decoding functions  $\varphi_1$ ,  $\varphi_2$  practically, decoding algorithms capable of handling errors and erasures can be used

for block codes, while suitably modified SOVA can be used for codes with trellis structure (e.g., for general PR channels, convolutional codes, etc.). In particular, binary erasures at the input of a SOVA decoder do not contribute to calculated path metric, while output erasures are generated for symbols whose reliability  $\Delta_j$  is below chosen threshold. The latter rule is based on the distance-based requirement for generating erasures in (24)–(26) and on an observation from [10] that  $\Delta_j$ is the distance between the maximum-likelihood decision **x** and the closest codeword (or message, depending on what we decode) differing from **x** in position *j*. The described AZD based iterative decoder easily extends to nonbinary codes and can be applied to decoding in various communication systems.

#### **III. APPLICATION OF ITERATIVE DECODERS**

Iterative decoding techniques from Section II are applied to several existing communications systems with concatenated structure. Since related standards specify the system transmitter side but leave the receiver implementation up to the manufacturers, the proposed solutions are backward compatible in practice. Each studied system is first briefly described, parameters of its transmitter are specified and simulation results of conventional (one-path) receiver and applied iterative decoder are compared.

# A. Existing Recording Systems

The main technical goals in digital recording product development are increased recording density, greater reading speed, higher reliability, and lower cost. Magnetic disk drives and tapes, digital audio tape, and digital video tape are important examples of magnetic recording applications, while compact disc, DVD, and rewritable DVD are among such optical recording systems. In the recording process of many of these systems, the data are first encoded by one or more error correcting encoders, followed by a modulation code and a partial response channel [16]-[18]. The error correcting codes are usually Reed-Solomon (RS) codes or their variants, e.g., shortened codes or product codes. The modulation code is usually a variation of a runlength-limited sequence code which guarantees clock recovery and alleviates the intersymbol interference (ISI) introduced by the channel [19], [20]. The channel with ISI is usually followed by an equalizing filter that confines the span of ISI within a reasonable number of symbols, thus creating, in effect, a partial response (PR) channel. For instance, in digital magnetic recording products the PR channel G(D) = 1 - D or  $1 - D^2$  describes the read out process. Conventionally used receivers for these systems use a one-path decoding structure, where the PR channel is usually followed by a maximum-likelihood decoder (combined scheme known as PRML proposed originally in [21]) followed by error correcting decoders.

We consider a recording system with serial concatenated structure from Fig. 1(b) which has parameters similar to those of the CD, DVD, and digital video tape. The first encoder is based on a (28, 24, 5) shortened RS code over  $GF(2^8)$  and the output bytes are interleaved by a  $28 \times 28$  block interleaver. The second encoder applies a (32, 28, 5) shortened RS code over the

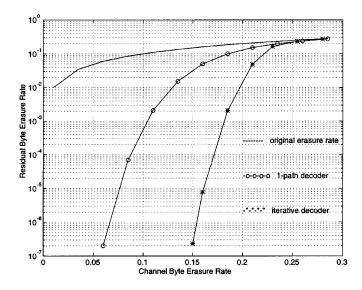


Fig. 3. Recording system performance curves for the conventional and iterative receiver on pure erasure channel.

same field and interleaves coded bytes by another block-based interleaver. Resulting data are passed onto effective channel which in our systems actually consists of a modulation encoder, PR channel with ML detection and an AZD detector. The modulation encoder maps bits corresponding to each byte into nine bits and these are passed onto a partial response channel. The PR channel includes the ISI channel and the equalizing filter so that a sequence x(D) that has passed through the PR channel is mapped into a sequence, which in polynomial form is given by

$$y(D) = x(D)(g_0 + g_1D + \dots + g_nD^n), \quad g_i \in \mathbb{R}.$$
 (28)

Channel noise corrupts the PR symbols and these are detected via maximum likelihood sequence estimation (MLSE) using the Viterbi algorithm over the PR trellis diagram. The decoded bits are then passed to the modulation decoder which uses redundancy introduced by modulation encoder to decide, whether the data have been received reliably or should be declared as "erasures" or ambiguous digits.

We have applied AZD based iterative decoder (23) from Section II-C2. Decoding functions  $\varphi_1, \varphi_2$  are based on RS decoding with erasures for appropriate RS codes. First, we tested the decoder on a somewhat simplified model of the recording channel with i.i.d. distributed byte-erasures due to the channel noise. In this case, RS decoders will not introduce any errors in the decoding process but they will be unable to decode if there are excessive erasures. The simulation results, shown in Fig. 3, depict the original erasure rate versus residual byte erasure rate. At residual byte erasure rate of  $10^{-6}$ , the iterative receiver can resolve about two and a half times as many erasures as the one-path receiver. Another way to interpret these results is that at the symbol erasure rate of 15% our decoder outperforms the existing decoder by almost five orders of magnitude. Note that the decoding converges after two to four iterations in most cases, thus making this iterative technique practical from the decoding delay point of view.

We then looked at the system performance assuming Gaussian noise corrupts duobinary PR symbols. Note that when the channel noise level is large, the AZD may introduce errors and

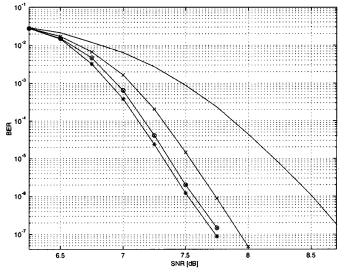


Fig. 4. Recording systems performance curves for channel with AWGN (right-to-left): Original one-path receiver and iterations 2–4.

the output data of the effective channel include correct and wrong bytes as well as erasures. The simulation results are shown in Fig. 4, which depicts the SNR versus residual error rate (BER). The four curves depict the performance after iterations one through four. The first iteration result is equivalent to the result of the conventionally used one-path receiver. For the channel SNR of 7.5 dB, the iterative receiver lowers the BER rate by a factor of almost 1000 after four iterations. Another way to view these results is that decoding gain of about one dB is achieved due to the iterative decoding at BER of  $10^{-6}$ . As the speed of read-out is a crucial bottleneck in data recording applications (e.g., rewritable DVDs, magnetic drives, digital magnetic tapes, etc.), achieved decoding gain can be traded for faster reading of the data, i.e., read head spending less time over given data symbols. For instance, one dB decoding gain corresponding to about 20% less needed signal power (assuming constant noise level) can be used to achieve faster data read out by up to 20%, and this can be achieved in a fully backward compatible manner with respect to existing standards. Finally, it is important to remark that the use of turbo codes and turbo decoding/equalization are the focus of active research on the next generation magnetic and optical recording systems. Papers presented by, for instance, Ryan et al. [23], Souvignier et al. [24], and Song et al. [25] contain results of ongoing effort in this area. Furthermore, there have been experimental studies on iterative turbo decoding of product codes based on binary block codes, e.g., for high rate Hamming codes using a neurocomputer [26] or using Chase algorithm [27] to achieve soft-decision decoding of BCH codes [28].

# B. GSM System

In the pan-European digital cellular standard Groupe Speciale Mobile (the Global System for Mobile Communication, or GSM), original error-correcting protection is based on a convolutional outer encoder, while Gaussian-filtered minimum shift keying (GMSK) is used as a bandwidth efficient modulation scheme [9]. GMSK is similar to traditional minimum shift keying (MSK) except that the baseband signal is passed through a Gaussian lowpass filter prior to frequency modulation so the signal spectrum is shaped to make the modulation signal spectrally efficient. In the time domain, this spreads the signal over multiple bit periods and introduces a controlled amount of ISI. Further ISI may be introduced to the received signal by multipaths in the radio channel and the overall transmitter has a serially concatenated structure given in Fig. 1(b), where the GMSK modulator together with the multipath channel act as the inner encoder. In this section, we apply iterative turbo decoder from equation (12), approach also referred to as turbo equalization [6], to improve the conventionally used one-path GSM receiver.

Encoder 1 is based on a rate 1/2 convolutional code with generator polynomials

$$g_1(D) = 1 + D^3 + D^4 \tag{29}$$

$$g_2(D) = 1 + D + D^3 + D^4 \tag{30}$$

which protects only the more important "Class I" bits, whereas the less important "Class II" bits are left unencoded [9]. Resulting data stream is interleaved using the interleaver described in [9] and a 26-bit training sequence

$$\underline{\alpha} = (001\ 001\ 011\ 100\ 001\ 000\ 100\ 101\ 11) \tag{31}$$

is inserted into the middle of each packet. The combined effects of the Gaussian filtering and the multipath channel can be modeled as a single impulse response h(t) of finite duration which is given by

$$h(t) = h_{\rm GMSK}(t) * h_{\rm chan}(t)$$
(32)

where \* denotes convolution. By sampling h(t) at t = kT (where T is the symbol interval), we obtain the discrete-time impulse response  $\{h_n\}$ . In particular, a binary sequence  $(a_1, a_2, \ldots), a_i \in \{0, 1\}$ , that enters the GMSK modulator and multipath channel, is transformed into a noiseless complex valued channel sequence  $\{s_n\}$ 

$$s_n = h_n * (j^{n+n_0} e^{j\pi a_n})$$
 (33)

where  $n_0 \in \mathbb{R}$  represents the initial phase offset of the modulator. Hence, Encoder 2 has a finite state machine structure and can be represented by a trellis diagram. [At the receiver, the time varying factor of  $j^n$  can be eliminated by multiplying the received symbols by  $j^{-n}$  and the effective channel parameters  $\{h_n\}$  are usually estimated using a matched filter based on the training sequence (31).]

Iterative decoder (12) has been applied to the described system with decoding functions  $\varphi_1$ ,  $\varphi_2$  implemented using SOVA algorithm. We have assumed AWGN corrupting channel symbols  $s_n$  from equation (33) and the effect of GMSK/multipath was modeled as a finite shift register encoder with five registers. Some of the simulation results are shown in Figs. 5 and 6 with decoding result after first iteration corresponding also to the conventional one-path GSM receiver. Decoding gain after four iterations is about 1.5 dB for convolutionally coded Class I bits. This gain can be utilized, for instance, by using less expensive analog radio frequency (RF) filters in manufactured GSM handsets. Lower SNR due to the worse performance of the filters is offset by the decoding gain and the same performance is achieved.

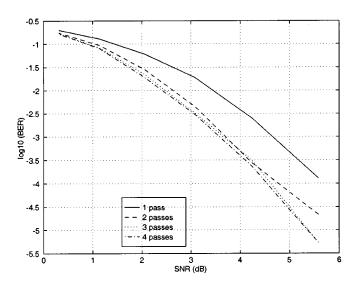


Fig. 5. Simulation results of the GSM system for multipath channel with two significant reflections.

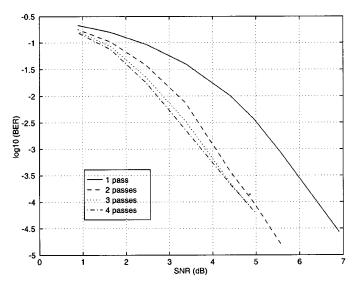


Fig. 6. Simulation results of the GSM system for multipath channel with three significant reflections.

# C. System with Transmitter Diversity

We consider an existing system used for transmission of paging messages [29]. A paging message for a given subscriber enters the system via an automated phone system, through an Internet web page, via e-mail, etc. Message packets are then transmitted over a digital channel which occupies an unused portion of the baseband spectrum in an FM radio carrier. Multiplexing of such digital channel and the FM audio channel is often referred to as subcarrier multiplexing. To achieve sufficiently high packet throughput, each packet is encoded and transmitted from at least three locations closest to a given subscriber with three repeated transmissions from each location. Thus, each paging message is transmitted at least nine times altogether and a receiver uses this information to recover the original message. The receiver size and complexity are strictly limited by the fact it is integrated as a part of a watch.

The current transmitter has a concatenated structure analogous to the one depicted in Fig. 7. Each source message with

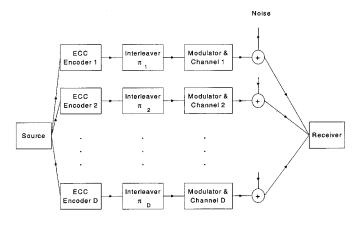


Fig. 7. Block diagram of considered paging system with transmitter diversity.

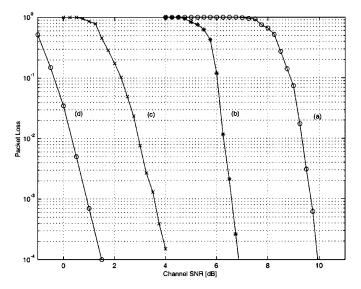


Fig. 8. Performance of the system with diversity. (a) The original transmitter and receiver. (b) The original transmitter and the iterative AZD based receiver. (c) Original transmitter and iterative AZD based receiver using decoded data from previous reception. (d) Improved transmitter and the receiver from (c).

160 information bits is encoded using a (12, 8) shortened Hamming code and resulting 240 bits are permuted by a  $20 \times 12$ block interleaver. Encoded data bits are passed to a differentially precoded duobinary modulator and transmitted nine times, i.e., D = 9 and  $\pi_1 = \pi_2 = \cdots = \pi_9$ . The originally designed receiver is based on bit-by-bit detection of the duobinary sequence and syndrome decoding of the (12, 8) Hamming code. If any of the nine independent transmissions of a message is successfully decoded, the message is successfully received. (Decision about successful reception of a decoded message is made at the sink using a cyclic redundancy check code already incorporated in the data from the source.) Note that due to its size limitations, this receiver cannot store channel data from various receptions and decoding starts afresh after each packet reception. The goal is to achieve overall packet loss of  $10^{-3}$  or less.

The simulation results on the AWGN channel are shown in Fig. 8 for four different systems. Curve (a) shows the performance for the original transmitter and receiver; Curve (b) shows the performance of the original transmitter, with the iterative AZD based decoder starting afresh at each reception. Curve (c) shows the original transmitter, as performing with the AZD based iterative receiver when successfully decoded bits are passed to the next reception, while the rest are marked as erased. Finally, it is also interesting to observe how much further improvement can be gained if improved interleaving is used. Interleavers from [30], with data written in a three-dimensional (3-D) array along one dimension, cyclically shifted by different amounts along the second dimension and read out along the third dimension, have been used and curve (d) shows the resulting performance. Improvement of almost nine dB is achieved for required *overall* packet loss of  $10^{-3}$ , six dB of which can be achieved in a backward compatible manner due to improved decoding (c). Both the duobinary decoder and Hamming decoder implementations are based on their trellis structures and are capable of performing decoding with erasures. The receiver introduces no additional delay in receiving good packets and requires little extra storage since the received channel data from previous receptions are not stored. It is also interesting to observe for the improved transmitter and receiver that the average number of receptions required for successful decoding increases with worsening of the channel SNR. At higher SNRs, the decoder is able to decode the message almost immediately, while a longer delay occurs at lower SNR since more receptions are needed to decode the packets successfully.

#### **IV. CONCLUSION**

In this paper, we have studied how backward compatible performance improvement can be achieved using iterative decoding in selected existing wireless communication and digital recording systems. Concatenated codes and iterative decoders were first described using composite mappings, so that various applications could be treated using a uniform notation. A novel AZD based iterative decoder was also introduced for concatenated systems where turbo decoding cannot be used. As simulation results have shown, decoding gain of up to several dB can be achieved with a relatively small increase in receiver computational complexity, since most of the decoding improvement is achieved within the first three to four iterations. The gains can be used to achieve faster data read-out, lower receiver cost or better coverage in the systems we studied.

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